

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		CDMA and FPGA and (chip near rate) and DSP and (symbol near rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 17:09
		("6801564").PN.	USPAT; USOCR	OR	OFF	2005/04/05 17:08
		CDMA and FPGA and (chip near rate) and DSP and (symbol near rate) and programmable	US-PGPUB; USPAT	OR	ON	2005/04/05 17:03
		"program"	US-PGPUB; USPAT	OR	ON	2005/04/05 16:57
		program	US-PGPUB; USPAT	OR	ON	2005/04/05 16:57
		(program and DSP)	US-PGPUB; USPAT	OR	ON	2005/04/05 16:56
		CDMA and FPGA and (chip near rate) and (program and DSP) and (symbol near rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 16:56
		CDMA and FPGA and (chip near rate) and program and DSP and (symbol near rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 16:55
		program and CDMA and FPGA and (chip near rate) and DSP and (symbol near rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 16:50
		L20 and program	US-PGPUB; USPAT	OR	ON	2005/04/05 16:49
		20 and "program"	US-PGPUB; USPAT	OR	ON	2005/04/05 16:49
		20 and program	US-PGPUB; USPAT	OR	ON	2005/04/05 16:48
L1	0	"20020027892"	USPAT	OR	ON	2005/04/05 08:50
L2	1	"20020027892"	US-PGPUB; USPAT	OR	ON	2005/04/05 09:27
L3	1154	CDMA and FPGA and DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 09:27
L4	961	CDMA and FPGA with DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 09:28
L5	2	CDMA and FPGA with DSP and TDD and FDD and MC-CDMA	US-PGPUB; USPAT	OR	ON	2005/04/05 09:29
L6	1	CDMA and FPGA with DSP and w-cdma and MC-CDMA	US-PGPUB; USPAT	OR	ON	2005/04/05 10:46
L7	3992	370/320,335,342,441.ccls.	US-PGPUB; USPAT	OR	ON	2005/04/05 10:49
L8	148	370/320,335,342,441.ccls. and CDMA and FPGA with DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 11:51

L9	1	370/320,335,342,441.ccls. and CDMA and FPGA with (chip adj rate) and DSP with (symbol adj rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 14:11
L10	1	370/320,335,342,441.ccls. and CDMA and (FPGA with (chip adj rate)) and (DSP with (symbol adj rate))	US-PGPUB; USPAT	OR	ON	2005/04/05 10:53
L11	1	370/320,335,342,441.ccls. and CDMA and (FPGA with (chip near rate)) and (DSP with (symbol near rate))	US-PGPUB; USPAT	OR	ON	2005/04/05 10:56
L12	3	CDMA and (FPGA with (chip near rate)) and (DSP with (symbol near rate))	US-PGPUB; USPAT	OR	ON	2005/04/05 14:12
L13	0	L8 and @ad<="2000905"	US-PGPUB; USPAT	OR	ON	2005/04/05 11:02
L14	148	L8 and @py<="2000905"	US-PGPUB; USPAT	OR	ON	2005/04/05 11:02
L15	7	L8 and @ad<="20000905"	US-PGPUB; USPAT	OR	ON	2005/04/05 11:02
L16	961	CDMA and FPGA with DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 11:51
L17	354	"370"/\$.ccls. and CDMA and FPGA with DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 11:51
L18	173	370/320,335,342,441.ccls. and CDMA and FPGA and DSP	US-PGPUB; USPAT	OR	ON	2005/04/05 11:51
L19	72	CDMA and (FPGA and (chip near rate)) and (DSP and (symbol near rate))	US-PGPUB; USPAT	OR	ON	2005/04/05 14:13
L20	72	CDMA and FPGA and (chip near rate) and DSP and (symbol near rate)	US-PGPUB; USPAT	OR	ON	2005/04/05 17:08